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Docket No.: 04303/100N146-US1
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Joel D. Medlock

Patent No.: 6,947,468

Issued: September 20, 2005

For: APPARATUS AND METHOD FOR
CALCULATING AND IMPLEMENTING A
FIBONACCI MASK FOR A CODE
GENERATOR

Certificate
FEB 06 2006
of Correction

REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 CFR 1.323 AND PATENT OFFICE MISTAKE (37 CFR 1.322)

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted several errors which should be corrected.

The following errors were not in the application as filed by applicant:

In the Specification:

Column 1, Line 17 (Approx.), Delete "DESPREADER" and insert

-- DESFREADER --.

Column 1, Line 32 (Approx.) After "2000" insert ---.

FEB 6 2006

Column 8, Line 57, Delete “6,567,071” and insert -- 6,567,017 --.

Column 8, Line 59, After “Medlock” insert ---.

Column 17, Line 63 (Approx.) Delete “claim” and insert -- Claim --.

Column 18, Line 40 (Approx.) In Claim 2, delete “step” and insert --steps--.

Column 18, Line 60 (Approx.) In Claim 2, after “is” delete “,”.

Enclosed please find copies of The Amendment & Response, Copies of the Specification, and copies of the Claims.

The following errors were found in the application as filed by applicant. The errors now sought to be corrected are inadvertent typographical errors. The correction of which does not involve new matter or require reexamination.

Column 3, Line 13 (Approx.), Delete “he” and insert --the--.

Column 3, Line 29, Delete “CMDA” and insert -- CDMA --.


Column 9, Line 22-23 (Approx.) After “information” insert ---.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

The Commissioner is authorized to charge any deficiency of up to \$300.00 or credit any excess in this fee to Deposit Account No. 04-0100. Enclosed please find a check for \$100.00

Dated: January , 2006

Respectfully submitted,

By 
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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 1 of 2

PATENT NO. : 6,947,468
APPLICATION NO. : 09/751,776
ISSUE DATE : September 20, 2005
INVENTOR(S) : Joel D. Medlock

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification:

Column 1, Line 17 (Approx.), Delete "DESPREADER" and insert

-- DESFREADER --.

Column 1, Line 32 (Approx.) After "2000" insert ---.

Column 3, Line 13 (Approx.), Delete "he" and insert --the--.

Column 3, Line 29, Delete "CMDA" and insert -- CDMA --.

Column 8, Line 57, Delete "6,567,071" and insert -- 6,567,017 --.

Column 8, Line 59, After "Medlock" insert ---.

Column 9, Line 22-23 (Approx.) After "information" insert ---.

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Column 17, Line 63 (Approx.) Delete "claim" and insert -- Claim --.

Column 18, Line 40 (Approx.) In Claim 2, delete "step" and insert --steps--.

Column 18, Line 60 (Approx.) In Claim 2, after "is" delete ",".

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AMENDMENTS TO THE SPECIFICATION

Replace the paragraph beginning on page 1, line 11, with the following:

A CONFIGURABLE ALL-DIGITAL COHERENT DEMODULATOR SYSTEM FOR SPREAD
SPECTRUM APPLICATIONS, Serial No. 09/751,783; ~~To Be Assigned, Attorney Docket No-~~
~~9824-0037-999~~

Replace the paragraph beginning on page 1, line 15, with the following:

A CONFIGURABLE MULTIMODE DESREADER FOR SPREAD SPECTRUM
APPLICATIONS, Serial No. 09/751,785; ~~To Be Assigned, Attorney Docket No. 9824-0036-999;~~

Replace the paragraph beginning on page 1, line 19, with the following:

A FAST INITIAL ACQUISITION & SEARCH DEVICE FOR A SPREAD SPECTRUM 2p
COMMUNICATION SYSTEM, Serial No. 09/751,777; ~~To Be Assigned, Attorney Docket No. 9824-~~
~~0033-999;~~

Replace the paragraph beginning on page 1, line 23, with the following:

FEB 6 2006

A CONFIGURABLE CODE GENERATOR SYSTEM FOR SPREAD SPECTRUM

~~APPLICATIONS, Serial No. 09/751,782, now Patent No. 6,567,017 To Be Assigned, Attorney~~
~~Docket No. 9824-029-999;~~

Replace the paragraph beginning on page 1, line 27, with the following:

METHOD AND APPARATUS TO SUPPORT MULTI STANDARD, MULTI SERVICE BASE-
STATIONS FOR WIRELESS VOICE AND DATA NETWORKS, Serial No. 09/752,050 To Be
~~Assigned, Attorney Docket No. 9824-0035-999; and~~

Replace the paragraph beginning on page 13, line 11, with the following:

IMPROVED APPARATUS AND METHOD FOR MULTI-THREADED SIGNAL PROCESSING,
Serial No. 09/492,634, filed on January 27, 2000.

Replace the paragraph beginning on page 11, line 16, with the following:

Referring now to Figure 3, a block diagram of a communication device for implementing a Fibonacci mask is shown, in accordance with one embodiment of the present invention. Electronic communication device 300 is a wireless code division multiple access (CDMA) base station for a

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cellular telephony application in the present embodiment. However, the present invention is well suited to use in a mobile handset, a test platform, an embedded wireless modem, other communication devices in the cellular telephony application, or any of the other wireless communication applications mention hereinabove. Furthermore, the present invention is applicable to any spread spectrum communication device utilizing code sequences. Communication device 300 is operable as described in a subsequent flowchart.

Replace the paragraph beginning on page 13, line 11, with the following:

While communication device 300 provides a specific quantity of components that are arranged in a specific configuration, the present invention is well suited to a wide range of alternatives. For example, Code generator 330 is a configurable code generator capable of generating codes for any one of multiple communication protocols. Additional detail on a configurable code generator is provided in co-pending US patent application serial number 09/751,782, now US patent number 6,567,017, entitled "A CONFIGURABLE CODE GENERATOR SYSTEM FOR SPREAD SPECTRUM APPLICATIONS" by Joel Medlock, attorney docket number 9824-0029-999. This related application is commonly assigned, and is hereby incorporated by reference.

contemplated. It is intended that the scope of the invention be defined by the Claims appended hereto and their equivalents.

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AMENDMENTS TO THE CLAIMS

Claims 1-13 (Canceled)

¹
Claim ~~14~~ (Currently Amended): In an electronic device having a processor coupled to a computer readable memory for implementing steps, a method of calculating a transform matrix for transforming a field vector from a second field to a field vector in a first field, the method comprising the steps of:

- a) receiving a reference code state chosen for the first field;
 - b) generating a first field vector of the reference code state;
 - c) iterating a linear feedback shift register (LFSR) state from the first field vector to form a new LFSR state;
 - d) generating a new field vector from the new LFSR state;
 - e) assembling the first field vector and the new field vector into ~~a~~ the transform matrix, the assembling step comprising the steps of:
 - e1) providing the first field vector as the bottom row in the transform matrix;
 - e2) providing the new field vector as the next highest row in the transform matrix;
- and

e3) repeating providing step e2) a total of N-2 times for a total of N rows in the transform matrix, wherein N is an integer; ^{greater than or equal to 2} and

f) repeating steps c) ~~through and~~ d) a quantity of N times, wherein N is a degree of a polynomial defining the first field and the second field.

Claims 15-33 (Canceled)

²
Claim ~~34~~ (Currently Amended): An electronic device for generating a mask for a linear feedback shift register (LFSR), the electronic device comprising;

(A:\00354932.RTF [REDACTED])

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a processor,

a computer readable memory unit coupled to the processor, the computer readable memory, containing program instructions stored therein that, when executed via the processor, implements a method of calculating a transform matrix for transforming a field vector from a second field to a field vector in a first field, the method comprising the steps:

- a) receiving a reference code state chosen for the first field;
- b) generating a first field vector of the reference code state;
- c) iterating an LFSR state from the first field vector;
- d) generating a new field vector from the new LFSR state;
- e) assembling the first field vector and the new field vector into a—the transform

- e1) providing the first field vector as the lowest row in the transform matrix;
- e2) providing the new field vector as the next highest row in the transform

e3) repeating providing step e2) a total of N-2 times for a total of N rows in the transform matrix, wherein N is an integer, and *greater than or equal to 2*

f) repeating steps c) ~~through~~ and d) a quantity of N times, wherein N is the a degree of ~~the~~ a polynomial defining the first field and the second field.

Claims 35-41 (Canceled)

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Attorney Docket No.: 04303/100N146-US1

Certificate of Express Mailing Under 37 CFR 1.10

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail, Airbill No. _____ in an envelope addressed to:

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on January³⁰, 2006
Date

B. W. Lee

Signature

B.W. CEE

Typed or printed name of person signing Certificate

Registration Number, if applicable

Telephone Number

Note: Each paper must have its own certificate of mailing, or this certificate must identify each submitted paper.

Certificate of Correction (2 pages)
Request for Certificate of Correction (3 pages)
Enclosed please find copies of The Amendment & Response (3 pages)
Copy of the Specification (1 page)
Copies of the Claims (2 pages)

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